

**IGBT Modules** 

### Power Module (V series) 1200V / 100A / 2-in-1 package

#### **■** Features

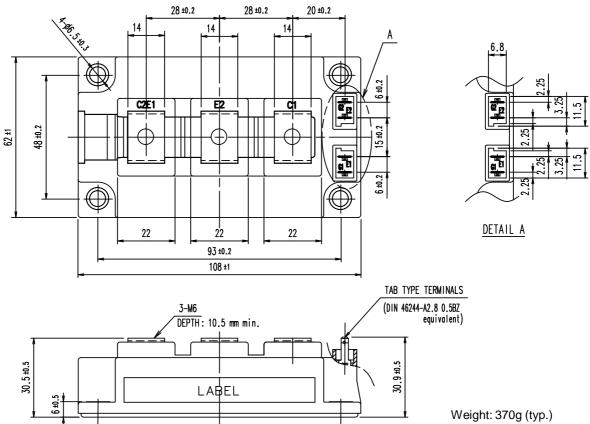
High speed switching Voltage drive Low Inductance module structure

### ■ Applications

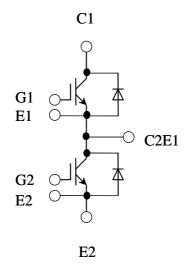
Soft-switching Application Industrial machines, such as Welding machines

### ■ Outline drawing (Unit: mm)





### **■** Equivalent Circuit



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### ■ Absolute Maximum Ratings (at T<sub>C</sub>= 25°C unless otherwise specified)

Items	Symbols	Conditions		Maximum Ratings	Units
Collector-Emitter voltage	V <sub>CES</sub>			1200	V
Gate-Emitter voltage	$V_{GES}$			±20	V
	ı	Continuous	Tc=60°C	100	
	I <sub>C</sub>		Tc=25°C	150	
Collector current	I <sub>C</sub> pulse	1ms		200	A
	-I <sub>C</sub>			400	
	-I <sub>C</sub> pulse	1ms		800	
Collector power dissipation	P <sub>C</sub>	1 device		655	W
Junction temperature	T <sub>j</sub>			150	
Case temperature	T <sub>c</sub>			125	°C
Storage temperature	T <sub>stg</sub>			-40 ~ 125	
Isolation between terminal and copper base	e V <sub>iso</sub>	AC: 1min.		2500	VAC
voltage (*1)	v iso	AC. IIIIII.		2300	VAC
Screw Mounting (*2)	-			6.0	N m
Torque Terminals (*3)	-			5.0	19 111

(\*1) All terminals should be connected together during the test.

(\*2) Recommendable Value: 3.0-6.0 Nm (M5 or M6)

(\*3) Recommendable Value: 2.5-5.0 Nm (M6)

**IGBT Modules** 

## ■ Electrical characteristics (at T<sub>j</sub>= 25°C unless otherwise specified)

ltomo	Symbolo	Conditions		Characteristics			Linita
Items Symbols		Conditions		min.	typ.	max.	Units
Zero gate voltage Collector current	I <sub>CES</sub>	VGE=0V, VCE=1200V		-	-	4.0	mA
Gate-Emitter leakage current	I <sub>GES</sub>	VCE=0V, VGE=±20\	/	-	-	800	nA
Gate-Emitter threshold voltage	$V_{GE(th)}$	VCE=20V, Ic=100m/	4	5.7	6.2	6.7	V
	V <sub>CE(sat)</sub>	VCE 45V In 100A	T <sub>j</sub> =25°C	-	3.60	3.90	
Collector-Emitter	(terminal)	VGE=15V, Ic=100A	T <sub>j</sub> =125°C	-	4.50	-	
saturation voltage	V <sub>CE(sat)</sub>	\\OF 45\\ \ \ 400A	T <sub>i</sub> =25°C	-	3.20	3.50	7
	(chip)	VGE=15V, Ic=100A	T <sub>i</sub> =125°C	-	4.10	-	1
Internal gate resistance	R <sub>G(int)</sub>	-	-		1.5	-	Ω
Input capacitance	C <sub>ies</sub>	V <sub>CE</sub> =10V, V <sub>GE</sub> =0V, f	=1MHz	-	7.6	-	nF
Turn-on time	t <sub>on</sub>			-	250	-	
	t <sub>r</sub>	$Vcc=600V$ $I_C=$	100A	-	180	-	1
	t <sub>r(i)</sub>	$V_{GE}$ = ±15V $R_{G}$ =	9.1Ω	-	40	-	nsec
Turn-off time	t <sub>off</sub>	$T_{j}$ = 125°C $L_{s}$ =	30nH	-	300	-	
	t <sub>f</sub>			-	50	-	
Forward on voltage	V <sub>F</sub>	\(\O_{\ounderline{\chi}}\)	T <sub>j</sub> =25°C	-	2.10	2.35	- V
	(terminal)	VGE=0V, IF=150A	T <sub>j</sub> =125°C	-	2.25	-	
	V <sub>F</sub>	\\OF 0\\ IF 4504	T <sub>i</sub> =25°C	-	1.70	1.95	
	(chip)	VGE=0V, IF=150A	T <sub>j</sub> =125°C	-	1.85	-	
Reverse recovery time	t <sub>rr</sub>	IF=100A		-	130	-	nsec

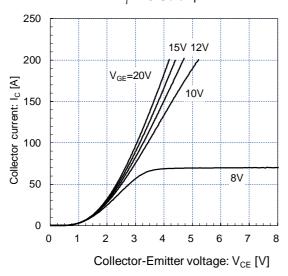
### 5. Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
			min.	typ.	max.	Units
Thermal resistance	P	IGBT	-	-	0.160	
(1device)	R <sub>th(j-c)</sub>	FWD	-	-	0.260	°C/W
Contact thermal resistance (1device) (*1)	R <sub>th(c-f)</sub>	with thermal compound	-	0.025	-	- C/VV

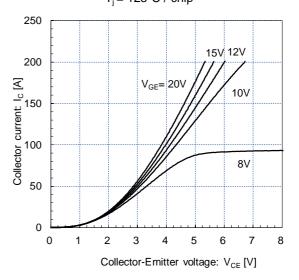
<sup>(\*1)</sup> This is the value which is defined mounting on the additional cooling fin with thermal compound.

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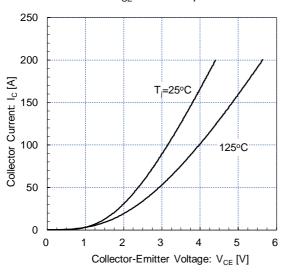
Collector current vs. Collector-Emitter voltage  $T_i = 25^{\circ}C$  / chip



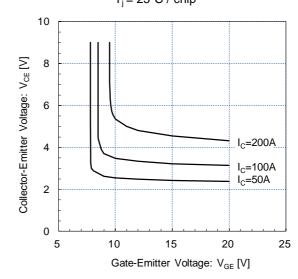
Collector current vs. Collector-Emitter voltage (typ.)  $T_i = 125$ °C / chip



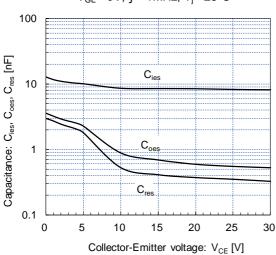
Collector current vs. Collector-Emitter voltage  $V_{GE} = 15V / chip$ 



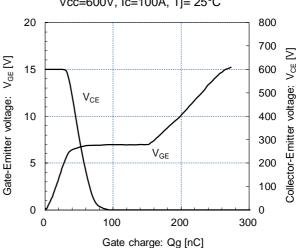
Collector-Emitter voltage vs. Gate-Emitter voltage  $T_i = 25^{\circ}C$  / chip



Capacitance vs. Collector-Emitter Voltage  $V_{GE}$ = 0V, f= 1MHz,  $T_i$ = 25 $^{\circ}$ C

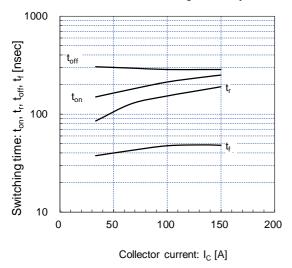


Dynamic Gate Charge (typ.) Vcc=600V, Ic=100A, Tj= 25°C

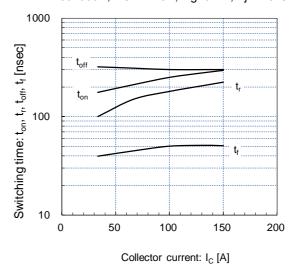


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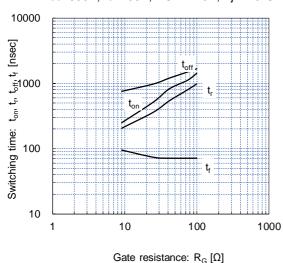
Switching time vs. Collector current (typ.) Vcc=600V, VGE= $\pm$ 15V, Rg= $9.1\Omega$ , Tj= $25^{\circ}$ C



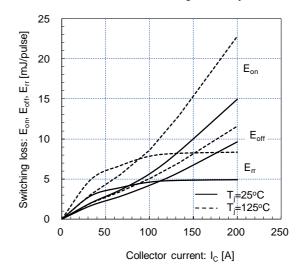
Switching time vs. Collector current (typ.) Vcc=600V, VGE=±15V, Rg=9.1Ω, Tj=125°C



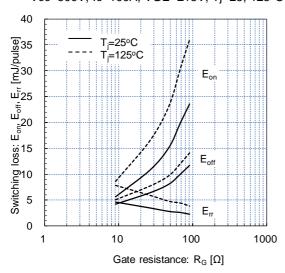
Switching time vs. Gate resistance (typ.) Vcc=600V, Ic=100A, VGE=±15V, Tj=125°C



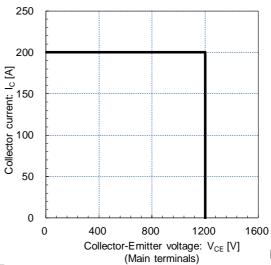
Switching loss vs. Collector current (typ.) Vcc=600V, VGE=±15V, Rg=9.1Ω, Tj=25, 125°C



Switching loss vs. Gate resistance (typ.) Vcc=600V, Ic=100A, VGE=±15V, Tj=25, 125°C



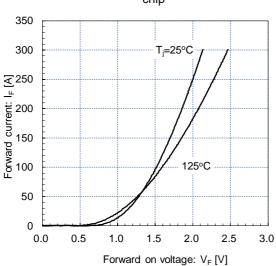
Reverse bias safe operating area (max.) +VGE=15V, -VGE=15V, Rg= $9.1\Omega$ , Tj= $125^{\circ}$ C



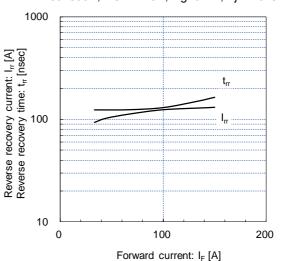


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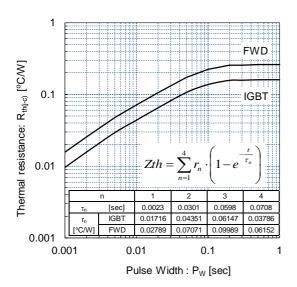
Forward current vs. Forward vltage (typ.) chip



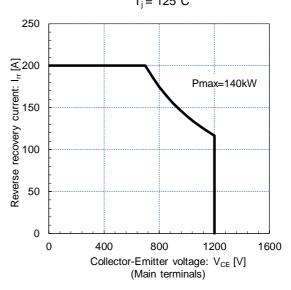
Reverse recovery characteristics (typ.) Vcc=600V, VGE=±15V, Rg=9.1Ω, Tj=125°C



Transient thermal resistance (max.)



FWD safe operating area (max.)  $T_i = 125$  °C



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